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PATENT COOPERATION Th_ATY

	From the INTERNATIONAL BUREAU
PCT	То:
NOTIFICATION OF ELECTION (PCT Rule 61.2)	Assistant Commissioner for Patents United States Patent and Trademark Office Box PCT Washington, D.C.20231 ETATS-UNIS D'AMERIQUE
Date of mailing (day/month/year) 06 September 2000 (06.09.00)	in its capacity as elected Office
International application No. PCT/GB99/04245	Applicant's or agent's file reference PDG/21095
International filing date (day/month/year) 15 December 1999 (15.12.99)	Priority date (day/month/year) 15 December 1998 (15.12.98)
Applicant ROBERTSON, Paul, Gordon	
1. The designated Office is hereby notified of its election made X in the demand filed with the International Preliminary 11 July 2000 (** in a notice effecting later election filed with the International Preliminary 11 July 2000 (**	y Examining Authority on: 11.07.00) national Bureau on:
The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland	Authorized officer Juan Cruz

Telephone No.: (41-22) 338.83.38

Facsimile No.: (41-22) 740.14.35 Form PCT/IB/331 (July 1992)

GB9904245



PATENT COOPERATION TREATY

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<u>) ,PC</u>)		PCT	

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's	or agent's file reference		See Notification of Transmittal of International
PDG/210	95	FOR FURTHER ACTION	Preliminary Examination Report (Form PCT/IPEA/416)
Internation	al application No.	International filing date (day/monti	n/year) Priority date (day/month/year)
PCT/GB	99/04245	15/12/1999	15/12/1998
Internation: H04N7/1		national classification and IPC	
SNELL 8	WILCOX LIMITED et al.		
	nternational preliminary exa s transmitted to the applicar		d by this International Preliminary Examining Authority
2. This	REPORT consists of a total	of 7 sheets, including this cover s	heet.
b (:	een amended and are the t	pasis for this report and/or sheets on 607 of the Administrative Instruct	ne description, claims and/or drawings which have containing rectifications made before this Authority ons under the PCT).
3. This	☐ Basis of the report	elating to the following items:	
II.	☐ Priority		
111	_		ventive step and industrial applicability
V			novelty, inventive step or industrial applicability;
VI	☐ Certain documents		
VII	⊠ Certain defects in the	e international application	
VIII		s on the international application	
Data of act	omission of the demand	Data of	completion of this report
Date of Sul	omission of the demand	Date of	Completion of this report
11/07/20	00	06.02.2	001
	mailing address of the internation examining authority: European Patent Office		zed officer
<i></i>	D-80298 Munich Tel. +49 89 2399 - 0 Tx: 523	656 epmu d	euleveult, A
	Fax: +49 89 2399 - 4465	T-1	20 No. 140 90 2200 9046

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No. PCT/GB99/04245

I. Ba	asis	of	the	re	port	t
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••						
1.	resp the	oonse to an invitati	Irawn on the basis of (substitute on under Article 14 are referred lo not contain amendments (Rul	to in this repo	ort as "originally filed".	
	1-10)	as originally filed			
	Clai	ims, No.:				
	14-2	22	as originally filed			
	1-13	3	as received on	11/01/2001	with letter of	08/01/2000
	Dra	wings, sheets:				
	1/4-	4/4	as originally filed			
2.			guage, all the elements marked international application was file			
	The	se elements were	available or furnished to this Aut	thority in the f	ollowing language:	, which is:
		the language of a	translation furnished for the pur	poses of the i	nternational search (u	under Rule 23.1(b)).
		the language of p	ublication of the international ap	plication (und	er Rule 48.3(b)).	
		the language of a 55.2 and/or 55.3).	translation furnished for the pur	poses of inter	national preliminary e	examination (under Rule
3.			cleotide and/or amino acid sec ry examination was carried out o			
		contained in the ir	nternational application in writter	n form.		
		filed together with	the international application in o	computer read	dable form.	
		furnished subsequ	uently to this Authority in written	form.		
		furnished subsequ	uently to this Authority in compu	ter readable f	orm.	
			at the subsequently furnished wr application as filed has been furr		ce listing does not go	beyond the disclosure in
		The statement that listing has been for	at the information recorded in co urnished.	mputer reada	ble form is identical to	the written sequence

4. The amendments have resulted in the cancellation of:

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No. PCT/GB99/04245

		the description,	pages:			
		the claims,	Nos.:			
		the drawings,	sheets:			
5.		This report has been considered to go bey) the amendments had not been made, since they have been (Rule 70.2(c)):
		(Any replacement sh report.)	eet contain	ing such	amen	dments must be referred to under item 1 and annexed to this
6.	Add	itional observations, i	f necessary	<i>/</i> :		
IV.	Lac	k of unity of invention	on			
1.	In re	esponse to the invitati	on to restri	ct or pay	additio	nal fees the applicant has:
		restricted the claims.				
		paid additional fees.				
		paid additional fees	under prote	st.		
	×	neither restricted no	paid additi	onal fees	•	
2.		This Authority found 68.1, not to invite the				ity of invention is not complied and chose, according to Rule additional fees.
3.	This	s Authority considers	that the req	uirement	of unit	ty of invention in accordance with Rules 13.1, 13.2 and 13.3 i
		complied with.				
	×	not complied with for see separate sheet		ng reasoi	ns:	
4.		nsequently, the follow mination in establishi			nationa	al application were the subject of international preliminary
		all parts.				
	Ø	the parts relating to	claims Nos.	1-8,22.		
V.		asoned statement ur itions and explanation				pard to novelty, inventive step or industrial applicability;
1.	Sta	tement				
	Nov	velty (N)	Yes:	Claims	1-8,2	2



International application No. PCT/GB99/04245

Inventive step (IS)

Yes: Claims

No:

Claims 1-8,22

Industrial applicability (IA)

Yes: Claims 1-8,22

No: Claims

2. Citations and explanations see separate sheet

VII. Certain defects in the international application

The following defects in the form or contents of the international application have been noted: see separate sheet

VIII. Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made: see separate sheet

EXAMINATION REPORT - SEPARATE SHEET

IV. Lack of unity

The separate groups of invention are:

- I. Claims 1-8, 22 (main invention)
- II. Claims 9-17
- III. Claim 18
- IV. Claims 19, 20
- V. Claim 21

They are not so linked as to form a single general inventive concept (Rule 13.1 PCT) for the following reasons:

- 1. Claims 1 and 8 are directed to a digital video signal processor for stripping timing reference signals (TRSs) from the digital video signal and inserting other timing reference signals at other locations.
 - Claim 22 is directed to the corresponding video signal.
 - NB: it is not quite clear which "scrambled" video signal is meant when referring back to claim 1.
- Claim 9 deals with a digital video interface wherein TRSs are transmitted less than twice per line.
 - NB: this claim appears not to be supported by the description. Besides, the way TRSs are defined with reference to digital video standards other than Recommendation 656 is not clear.
- 3. Claim 18 is directed to a digital video interface for scrambling data words wherein specific words are re-scrambled until a valid word is obtained.
- 4. Claim 19 is directed to a digital video interface for scrambling data words wherein specific words are replaced with unscrambled words.
- 5. Claim 21 deals with a digital video processor for inserting into the video signal identical TRSs appearing once per picture.
 - NB: no support is to be found in the description for this claim either.

V. Reasoned statement

1. Reference is made to the following document:

D1: US-A-5 757 910 (RIM) 26 May 1998

2. Claim 1:

Document D1 discloses (see column 3, line 47 - col. 5, l. 46) a digital video processor (120-150) having an input adapted to receive a digital video signal (output from A/D converter 110) and a timing reference processor (130) for inserting timing signal references into the video signal (see the paragraph bridging columns 4 and 5).

Furthermore, it is well known to the skilled person that a video signal (analog or digital) normally comprises first timing signal references at fixed first locations within the line and picture structure (see for instance the "horizontal and vertical synchronous signals" in col. 1, I. 18-23 of D1) and that, for the purpose of scrambling, a timing reference stripper is required to remove said first timing signal references from the video signal (see col. 1, I. 52-57) before said timing reference processor can insert (second) timing signal references into the video signal at locations other than said first locations.

Whether such a timing reference stripper processes an analog or a digital video signal, these are obvious alternatives for the skilled person who would select one or the other, in accordance with circumstances, without the exercise of inventive skill, in order to scramble the video signal.

Consequently, the claimed subject-matter is considered to lack an inventive step with respect to the disclosure of D1.

3. Claim 8:

D1 further discloses a digital video processing system (see figures 3 and 4 together) comprising, in addition to an output component according to claim 1, an input component (210-270) adapted to receive (see antenna A) a digital video signal having said second timing signal references, to remove (230) said second timing signal references and to derive (250) from said second timing signal references appropriate timing references for use in further processing (260) of the digital video signal.

This claim thus does not satisfy the requirements of Article 33(3) PCT either.

4. Claim 22:

The same objection likewise applies to the video signal itself.

INTERNATIONAL PRELIMINARY International application No. PCT/GB99/04245 EXAMINATION REPORT - SEPARATE SHEET

5. Claims 2-7:

These dependent claims do not appear to comprise any additional features that would render their subject-matter inventive over the available prior art.

Therefore, these claims fail together with the independent claims for lack of inventive step.

VII. Certain defects

- 1. The independent claims are not in the two-part form in accordance with Rule 6.3(b) PCT, which in the present case would be appropriate, with those features known in combination from the prior art (document D1) being placed in the preamble (Rule 6.3(b)(i) PCT) and with the remaining features being included in the characterising part (Rule 6.3(b)(ii) PCT).
- 2. The features of the claims are not provided with reference signs placed in parentheses (Rule 6.2(b) PCT).
- 3. Contrary to the requirements of Rule 5.1(a)(ii) PCT, the relevant background art disclosed in the document D1 is not mentioned in the description, nor is this document identified therein.

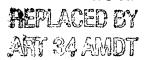
VIII. Certain observations

- 1. Claims 1 through 8 comprise "timing signal references" whereas the description refers to "timing reference signals (TRSs)".
- 2. Claim 22 does not meet the requirements of Article 6 because it is not clear which "scrambled" video signal is meant when referring back to claims 1 and 8.

CLAIMS

- 1. A digital video signal processor having an input adapted to receive a digital video signal having first timing signal references at fixed first locations within the line and picture structure; a timing reference stripper for removing said first timing signal references from the digital video signal and a timing reference processor for inserting second timing signal references into the video signal at locations other than said first locations.
- A processor according to Claim 1, wherein said second timing signal references are substantially fewer in number than said first timing signal references.
- 3. A processor according to Claim 1, wherein said first timing signal references comprise start of line and end of line references and said second timing signal references are inserted substantially once per picture.
- 4. A processor according to any one of the preceding claims, wherein substantially every second timing signal reference includes information concerning the number of lines per picture within the digital video signal.
- 5. A processor according to any one of the preceding claims, wherein substantially every second timing signal reference includes information concerning the length of each line within the digital video signal.
- 6. A processor according to any one of the preceding claims, wherein substantially every second timing signal reference includes information concerning the aspect ratio of the picture.

- 7. A processor according to any one of the preceding claims, wherein substantially all the second timing signal references are identical.
- 8. A digital video signal processing system comprising an output component adapted to receive a digital video signal having first timing signal references at fixed first locations within the line and picture structure, to remove said first timing signal references and insert second timing signal references at locations other than said first locations; and an input component adapted to receive a digital video signal having said second timing signal references, to remove said second timing signal references and to derive from said second timing signal references appropriate timing references for use in further processing of the digital video signal.
- 9. A digital video interface substantially in accordance with ITU/R Recommendation 656, or other digital video standard, characterised in that timing reference signals are transmitted less than twice per line in order to inhibit unauthorised use of the video information.
- 10. A digital video interface as described in Claim 9 where no timing reference signal corresponds to the start or finish of a digital active line.
- 11. A digital video interface in accordance with either Claim 9 or Claim 10 in which the timing reference signals are identical.
- 12. A digital video interface in accordance with either Claim 9 or Claim 10 in there is no explicit F, V and H information in the timing reference signals.
- 13. A digital video interface in accordance with any one of Claims 9 to 12, in which aspect ratio information is carried in the timing reference signals.



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CLAIMS

- 1. A digital video signal processor having an input adapted to receive a digital video signal having first timing signal references at fixed locations within the line and picture structure; a timing reference stripper for removing said first timing signal references from the digital video signal and a timing reference processor for inserting second timing references into the video signal at locations other than said first locations.
- 2. A processor according to Claim 1, wherein said second timing references are substantially fewer in number than said first timing references.
- A processor according to Claim 1, wherein said first timing signal references comprise start of line and end of line references and said second timing references are inserted substantially once per picture.
- A processor according to any one of the preceding claims, wherein substantially every second timing reference includes information concerning the number of lines per picture within the digital video signal.
- 5. A processor according to any one of the preceding claims, wherein substantially every second timing reference includes information concerning the length of each line within the digital video signal.
- A processor according to any one of the preceding claims, wherein substantially every second timing reference includes information concerning the aspect ratio of the picture.

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- 7. A processor according to any one of the preceding claims, wherein substantially all the second timing references are identical.
- 8. A digital video signal processing system comprising an output component adapted to receive a digital video signal having first timing signal references at fixed locations within the line and picture structure, to remove said first timing signal references and insert second timing references at locations other than said first locations; and an input component adapted to receive a digital video signal having said second timing signal references, to remove said second timing signal references and to derive from said second timing references appropriate timing references for use in further processing of the digital video signal.
- 9. A digital video interface substantially in accordance with ITU/R Recommendation 656, or other digital video standard, characterised in that timing reference signals are transmitted less than twice per line in order to inhibit unauthorised use of the video information.
- A digital video interface as described in Claim 9 where no timing reference signal corresponds to the start or finish of a digital active line.
- 11. A digital video interface in accordance with either Claim 9 or Claim 10 in which the timing reference signals are identical.
- A digital video interface in accordance with either Claim 9 or Claim 10 in there is no explicit F, V and H information in the timing reference signals.
- 13. A digital video interface in accordance with any one of Claims 9 to 12, in which aspect ratio information is carried in the timing reference signals.

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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(72) Inventor; and

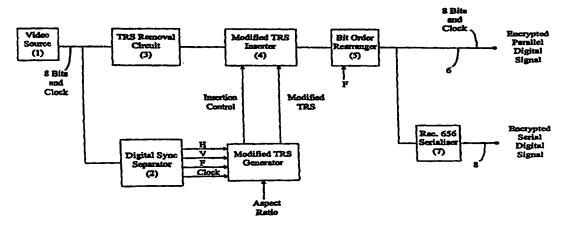
(75) Inventor/Applicant (for US only): ROBERTSON, Paul, Gordon [GB/GB]; 197 The Causeway, Petersfield, Hampshire GU31 4LN (GB).

(74) Agents: GARRATT, Peter, Douglas et al.; Mathys & Squire, 100 Gray's Inn Road, London WC1X 8AL (GB). (81) Designated States: AU, CA, JP, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published

Without international search report and to be republished upon receipt of that report.

(54) Title: DIGITAL VIDEO PROCESSING



(57) Abstract

To inhibit unauthorised copying, the standard timing reference signals in a Rec 656 digital video signal are stripped out and replaced by a single timing reference per frame. Authorised equipment contains the processing to reconstruct the necessary timing from these timing references. Additionally, the digital words can be scrambled by bit re-ordering or by a more sophisticated process.

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INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)

Applicant's or agent's file reference	FOR FURTHER see Notification o	f Transmittal of International Search Report
PDG/21095	ACTION (Form PCT/ISA/2)	20) as well as, where applicable, item 5 below.
International application No.	International filing date (day/month/year)	(Earliest) Priority Date (day/month/year)
PCT/GB 99/04245	15/12/1999	15/12/1998
Applicant		
SNELL & WILCOX LIMITED et	al.	
This International Search Report has been	n prepared by this International Searching Auth	ority and is transmitted to the applicant
according to Article 18. A copy is being tra	ansmitted to the International Bureau.	
This International Search Report consists It is also accompanied by	of a total of 2X sheets. a copy of each prior art document cited in this	report.
Basis of the report		
 a. With regard to the language, the language in which it was filed, unl 	international search was carried out on the bas ess otherwise indicated under this item.	is of the international application in the
the international search w Authority (Rule 23.1(b)).	as carried out on the basis of a translation of th	e international application furnished to this
b. With regard to any nucleotide an was carried out on the basis of the	d/or amino acid sequence disclosed in the int	ernational application, the international search
	nal application in written form.	
filed together with the inte	rnational application in computer readable form	ı.
furnished subsequently to	this Authority in written form.	
furnished subsequently to	this Authority in computer readble form.	
the statement that the sub international application a	sequently furnished written sequence listing do s filed has been furnished.	es not go beyond the disclosure in the
the statement that the info furnished	rmation recorded in computer readable form is	identical to the written sequence listing has been
Certain claims were four	nd unsearchable (See Box I).	•
3. Unity of Invention is laci	ding (see Box II).	
4. With regard to the title,		
the text is approved as su	bmitted by the applicant.	
	ned by this Authority to read as follows:	
DIGITAL VIDEO PROCESSI	NG TO INHIBIT UNAUTHORISED	COPIES
5. With regard to the abstract,		
the text is approved as sul	omitted by the applicant.	
the text has been establish	ned, according to Rule 38.2(b), by this Authority date of mailing of this international search repo	
6. The figure of the drawings to be publi	shed with the abstract is Figure No.	1
$oxed{X}$ as suggested by the applic		None of the figures.
because the applicant faile	ed to suggest a figure.	
because this figure better	characterizes the invention.	



International Application No PCT/GB 99/04245

A CLASSI	FIGATION OF OUR IFOT MATTER		
IPC 7	FICATION OF SUBJECT MATTER H04N7/169 H04N7/171		
	o International Patent Classification (IPC) or to both national classific	cation and IPC	
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Documental	tion searched other than minimum documentation to the extent that	such documents are included in the fields se	earched
Electronic d	ata base consulted during the international search (name of data ba	ase and, where practical, search terms used)
WPI Da	ta, PAJ		
C. DOCUM	ENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of the rel	levant passages	Relevant to claim No.
_			
Α	US 5 757 910 A (RIM)		1–22
	26 May 1998 (1998-05-26) column 1, line 52 -column 2, line	e 64	
	column 3, line 47 -column 5, line		
	figures 3-5		
P,A	EP 0 949 815 A (NEC CORPORATION)		15-22
, ,	13 October 1999 (1999-10-13)		
	column 6, line 6 -column 7, line	44;	
	figure 1 		
Furth	ner documents are listed in the continuation of box C.	X Patent family members are listed	n annex.
° Special cat	tegories of cited documents :	"T" later document published after the inter	
	nt defining the general state of the art which is not ered to be of particular relevance	or priority date and not in conflict with cited to understand the principle or the	the application but
"E" earlier d	ocument but published on or after the international	invention "X" document of particular relevance; the cl	
	nt which may throw doubts on priority claim(s) or	cannot be considered novel or cannot involve an inventive step when the do	cument is taken alone
citation	s cited to establish the publication date of another or other special reason (as specified)	"Y" document of particular relevance; the cl cannot be considered to involve an inv	entive step when the
other n		document is combined with one or mo ments, such combination being obviou in the art.	
"P" docume later th	nt published prior to the international filing date but an the priority date claimed	"&" document member of the same patent f	amily
Date of the a	actual completion of the international search	Date of mailing of the international sea	rch report
10	July 2000	14/07/2000	
Name and m	nailing address of the ISA	Authorized officer	
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ERNATIONAL SEARCH REPORT

Information on patent family members

Internationa	Application No	•
PCT/GB	99/04245	

, Patent document cited in search repor	t	Publication date		Patent family member(s)	Publication date
US 5757910	Α	26-05-1998	KR KR	9611031 B 9708408 B	16-08-1996 23-05-1997
EP 0949815	Α	13-10-1999	JP	11298878 A	29-10-1999